Online DCIR Estimation for Series-connected Battery Cells using Matrix-Switched Capacitor Converter
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ABSTRACT
In the battery energy storage system, battery cells are connected in series to increase the operating voltage. Due to the difference in characteristics, the performance degradation of cells is dissimilar. This paper proposes an online DC internal impedance estimation for battery cells in the series string using a matrix-switched capacitor converter, which is already verified as useful for the series balancing of the cells. The simulation in the hardware in the loop test rig shows good accuracy and the feasibility of the proposed method.

Keywords: battery energy storage system, online DC internal impedance, hardware in the loop test rig, series-connected battery, Matrix-switched capacitor converter.

1. INTRODUCTION
To increase the operating voltage of the battery pack in electric vehicles or energy storage systems, multiple battery cells are connected in series. To ensure safety, the state of charge (SOC) and state of health (SOH) of battery cells are monitored regularly. When the SOC can be assessed by the degradation of battery cells, the DC Internal Resistance (DCIR) of battery cells is monitored. Various impedance estimation methods are reported in [1], where the methods are classified into offline and online measuring techniques.

In [2, 3], a switched inductor converter is used to inject an AC signal into battery cells and the electrochemical impedance of the battery is estimated. However, it is hard to delimit the impedance value between 2 battery cells. On the other hand, the battery charger is utilized at the end of the charging process to scan the battery impedance [4]. Unfortunately, it only can apply for one battery at the time. To reduce the cost, an online impedance spectroscopy estimation using switched capacitor cell balancing is proposed [5]. Although the accuracy is high, the incurred cost for the voltage and current sensors is its disadvantage.

This paper proposes an online DCIR estimation for series-connected battery cells by utilizing the matrix-switched capacitor equalizer which is suggested in [6]. This paper introduces the operation principle of the DCIR estimation technique in section 2, hardware in the loop test results are performed in section 3, and the conclusion is made in section 4.

2. PROPOSED METHOD
The proposed method uses one matrix-switched capacitor converter as shown in Fig. 1. Besides, one current sensor and one voltage sensor are used to measure the balancing current and the capacitor voltage, respectively. To discharge the capacitor voltage to zero, a dummy load and a switch are used to discharge the capacitor in the second state of the measuring process. With the matrix-switch, the capacitor can connect with any battery cell in the series string. Based on the measured transferring current and the capacitor voltage, the DCIR of the battery is estimated.

The equivalent circuit of a measuring process is presented in Fig. 2(a) and the switches are controlled by a complementary PWM signal as in Fig. 2(b). The measuring process is divided into two states: measuring state – Phase A \((t_0 – t_1)\) and recalibrating state – Phase B \((t_2 – t_3)\). Between two-state, a small deadtime period is set to prevent the short-circuit. To analyze the operation of the circuit, the circuit is transformed to s-domain which is represented in Fig. 3, where \(R_x\) is the total resistance of the loop (on-resistance of the switch, \(R_{on}\), battery impedance, \(R_b\)), \(C\) is the capacitance of the measuring capacitor, and \(V_c\) is the voltage of the capacitor. In phase A, switch \(S_1\) is turned on while \(S_2\) is kept off. Denote the time constant, \(\tau_1\), of the switched capacitor is calculated by (1), where \(R_x\) is calculated by (2).

\[
\tau_1 = R_x C \tag{1}
\]

\[
R_x = R_b + R_{on} + ESR \tag{2}
\]
The current flowing into the measuring loop, $I_{ts}$, is calculated by (3), where the $\Delta V$ is the difference between the battery voltage and the initial voltage of the capacitor as in (4).

$$I_{ts} = \frac{V_c(t_0) - V_c(t)}{R_s} = \frac{\Delta V}{R_s} = \frac{1}{s + \frac{1}{R_s C}}$$

(3)

$$\Delta V = V_c(t_0) - V_c(t)$$

(4)

By inverse-transforming to the time domain, the transferring current equation becomes (5). Thus, the stored charge in phase A is calculated by (6).

$$i(t) = \frac{\Delta V}{R_s} e^{-\frac{t}{R_s C}}$$

(5)

$$Q(t) = \int_{t_0}^{t} i(t) dt = \Delta V C (1 - e^{-\frac{t}{R_s C}})$$

(6)

Next, the voltage of the capacitor at time $t_1$ can be calculated by (7), where $V_c(t_0)$ is expected to be zero.

$$V_c(t_1) = \frac{Q}{C} = \Delta V (1 - e^{-\frac{t_1}{R_s C}}) + V_c(t_0)$$

(7)

By dividing (5) by (7), a function by $R_s$ calculated as (13), where $f(G)$ is the switching frequency of the switched capacitor converter.

$$R_{dummy} < \frac{1}{f \cdot C}$$

(13)

When the measuring process of one cell is finished, the switching decision is changed to assess the next cell in the string. As a result, the DCIRs of all cells can be estimated.

3. VERIFICATION OF THE PROPOSED METHOD

To verify the proposed method, hardware in the loop test for four series-connected 18650 battery cells (3.7V/2.6Ah) has been implemented. Assume that all cells are fully charged but the DCIRs of them are different as in Table I. The capacitance is set at 6800uf, the sum of $R_{d, on}$ and ESR is set at 100mΩ. Besides, the $R_{dummy}$ is set as 20mΩ to discharge the capacitor voltage to zero. The estimated DCIRs of battery cells are summarized in Table I. The results show that the proposed method can estimate the individual DCIR of battery cells within 5.5% tolerance.

4. CONCLUSION

This paper proposes an online DCIR estimation technique based on the matrix-switch capacitor converter for series-connected battery cells. With the matrix-switch structure, the proposed method can estimate the individual DCIR value of battery cells and equalize the energy between cells. The HIL test shows that the tolerance of the method is within 5.5%. A hardware experiment is under preparation for further verifying of the proposed method.

REFERENCES


