

# KIPA IT-SoC사업단

한국소프트웨어진흥원 IT-SoC사업단 IP 보유 목록 (상용구매 IP 총 28종 보유)

- ARM/DSP 코어 및 Interface IP

IP명	제조사	기능	비고
ARM Core 922T	ARM	RISC CPU Core	netlist & GDS
ARM Core 946E	ARM	RISC CPU Core	netlist & GDS
ARM ADK (AMBA Design Kit)	ARM	AMBA Design Kit	VHDL/Verilog
DSP Core Teak	Ceva	Teak DSP Core	netlist & GDS
DSP Core TeakLite	Ceva	TeakLite DSP Core	netlist & GDS
DSP Core Palm	Ceva	Palm DSP Core	netlist & GDS
PCI Master/Target	FTD	PCI 33MHz, 32bit	VHDL/Verilog
IEEE 1394 Link Layer Cont.	FTD	IEEE 1394a, 400Mbps	VHDL/Verilog
USB 1.1 Function Core	FTD	Full Speed 12Mbps, Low Speed 1.5Mbps	VHDL/Verilog
PCI 66MHz/64bit	CAST	Interface	Verilog
Ethernet MAC	CAST	Interface	VHDL
Interface	클로트릭스	USB2.0	Verilog

- RTL 소스는 제공할 수 없으며, 컴파일된 라이브러리 형태로 제공
- PSP : Processor Simulation Package

- ARM Primecell

IP명	기능	비고
ARM PrimeCell UART	UART	VHDL/Verilog
ARM PrimeCell SMC	Static Memory Controller	VHDL/Verilog
ARM PrimeCell SDRAM	SDRAM Controller	VHDL/Verilog
ARM PrimeCell RTC	Real Time Clock	VHDL/Verilog
ARM PrimeCell GPIO	General Purposed Input/Output	VHDL/Verilog
ARM PrimeCell SSPMS	Synchronous Serial Port Master/Slave	VHDL/Verilog
ARM PrimeCell SCI	Smart Card Interface	VHDL/Verilog
ARM PrimeCell DCDC	DC-DC Converter	VHDL/Verilog
ARM PrimeCell AACI	Advanced Audio Codec Interface	VHDL/Verilog
ARM PrimeCell KMI	Keyboard/Mouse Interface	VHDL/Verilog
ARM PrimeCell DMAC	Direct Memory Access Controller	VHDL/Verilog
ARM PrimeCell VIC	Vector Interrupt controller	VHDL/Verilog
ARM PrimeCell EBI	External Bus Interface	VHDL/Verilog
ARM PrimeCell CLCDC	Color LCD Controller	VHDL/Verilog
ARM PrimeCell MPMC	Multi-port Memory Controller	VHDL/Verilog
ARM PrimeCell MMC	Multimedia Card Interface	VHDL/Verilog

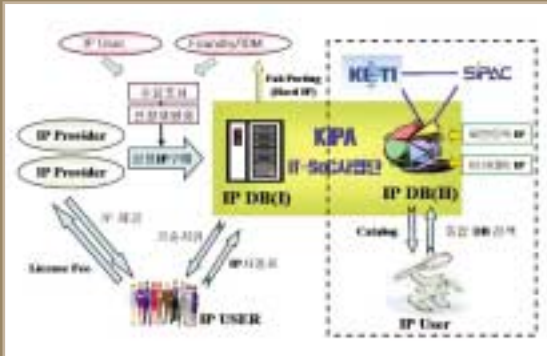
## SIPAC

2004년 8월부터 10월 초까지 43개의 IP가 SIPAC에 새로 등록되었습니다. 지면관계상 19개 IP의 정보를 제공해 드리며, SIPAC 홈페이지(<http://www.sipac.org>)를 방문하시면 보다 다양하고 자세한 IP 정보를 보실 수 있습니다. (Total: 874개)

No.	IP Name (Type / Format)	Seller	Category
1	WILD 802.11 a/b/g MAC (Soft IP / VHDL)	New Logic ( <a href="http://www.newlogic.com">http://www.newlogic.com</a> )	Wireless Communications
2	Voltage Down Converter for low-voltage SoC (Hard IP / GDSII)	Changwon National University ( <a href="http://www.changwon.ac.kr">http://www.changwon.ac.kr</a> )	Analog & Mixed Signal → Converter
3	Viterbi decoder (2, 1/2 decoder) (Soft IP / VHDL)	Catholic University ( <a href="http://www.catholic.ac.kr">http://www.catholic.ac.kr</a> )	Data Transmission
4	SATA II rev. 1.1 eVC - HDH 2000(Soft IP / Other[Specman e])	HDL Design House ( <a href="http://www.hdl-dh.com">http://www.hdl-dh.com</a> )	Verification Component
5	Analog Memory Cell Fabricated in a 0.35μm Single Poly Standard CMOS Process(Firm IP / GDS II)	Keimyung University ( <a href="http://www.kmu.ac.kr">http://www.kmu.ac.kr</a> )	Analog & Mixed Signal
6	SPCOTG20(Soft IP / Verilog)	Specsoft (Division of Hexaware Technologies) ( <a href="http://www.specsoftus.com">http://www.specsoftus.com</a> )	Data Transmission
7	4350 Hardware CIF MPEG4 / H.263 Codec (Soft IP / VHDL, Verilog)	Hantro Products OY ( <a href="http://www.hantro.com">http://www.hantro.com</a> )	Digital Signal Processing → Video Encoder/Decoder
8	Rotary machine receiver with detecting a header on the asynchronous serial communication (Soft IP / VHDL)	Dong-a University ( <a href="http://www.donga.ac.kr">http://www.donga.ac.kr</a> )	Data Transmission
9	H/W Design of SEED algorithm (Soft IP / VHDL)	Anyang University ( <a href="http://www.anyang.ac.kr">http://www.anyang.ac.kr</a> )	Data Transmission → Encryption → Others
10	DLL-based frequency synthesizer (Hard IP / GDS II)	Kookmin University ( <a href="http://www.kookmin.ac.kr">http://www.kookmin.ac.kr</a> )	Analog & Mixed Signal → PLL/DLL

한국소프트웨어진흥원(KIPA) IT-SoC사업단 IP 지원 체계

- SoC 중소설계기업의 요구 IP 수요조사에 의거하여 IP 구매 예정 목록을 만듭니다. 이를 바탕으로 선정 위원회를 개최하여 최종 구매 IP를 선정
- 구매한 IP는 KIPA IP DB에 등록되어 관리
- USER는 IP DB를 검색하여 사용하고자 하는 IP를 KIPA에 신청하면, KIPA가 기술지원을 제공



# IP CATALOG

No.	IP Name (Type / Format)	Seller	Category
11	Rijndael, 128-bit Block Cipher Algorithm (Soft IP / VHDL)	Handong Global University ( <a href="http://www.handong.edu">http://www.handong.edu</a> )	Processor & Micro-controller
12	PCI Controller (Soft IP / Verilog)	Chungnam National University ( <a href="http://www.cnu.ac.kr">http://www.cnu.ac.kr</a> )	Peripheral Core
13	SpectraChip Digital IF (Firm IP / EDIF, Bitstream)	RF Engines Ltd. ( <a href="http://www.rfel.com">http://www.rfel.com</a> )	Digital Signal Processing
14	SoCBase(Single processor based platform)	Seoul Natioanl University ( <a href="http://www.snu.ac.kr">http://www.snu.ac.kr</a> )	Platform IP
15	CoolFluxDSP (Soft IP / VHDL, Verilog)	Philips Digital Systems Laboratories Leuven (PDSL) ( <a href="http://www.bangalore.philips.com">http://www.bangalore.philips.com</a> )	Digital Signal Processing
16	65co2 compatible processor (Soft IP, Firm IP / VHDL, EDIF)	Sierra Circuit Design ( <a href="http://sierracircuit.8m.com">http://sierracircuit.8m.com</a> )	Processor & Micro-controller → Microprocessor → CISC 8-Bit
17	MPEG4 / H.263 Video Encoder Core (Firm IP / VHDL)	4i2i Communications Ltd ( <a href="http://www.4i2i.com">http://www.4i2i.com</a> )	Digital Signal Processing → Video Encoder/Decoder
18	MAC-to-PLCP Interface for IEEE 802.11 (Soft IP / VHDL)	Chonbuk National University ( <a href="http://www.cbu.ac.kr">http://www.cbu.ac.kr</a> )	Networking
19	H.264 INVERSE INTEGER TRANSFORM (Soft IP / Verilog)	PixSil Technology ( <a href="http://www.pixsiltech.com">http://www.pixsiltech.com</a> )	Digital Signal Processing