# Design of a Planar Cavity Resonator for 12.5 GHz Low Phase Noise SiGe HBT Oscillator

Jae-Won Lee<sup>1</sup> · Yong-Hoon Kim<sup>2</sup>

#### Abstract

In this paper, the novel microwave oscillator incorporating a planar cavity resonator(PCR) is presented to reduce the phase noise of the oscillator in a planar environment. Compared to the conventional planar( $\lambda/4$  open stub resonator), the phase noise is improved about 16 dBc/Hz @100 kHz. The design of the oscillator is based on a reflection type configuration using the low 1/f SiGe HBT transistor(LPT16ED). The output power is measured 2.76 dBm at 12.5 GHz. In this paper, the oscillator used to the PCR can be expected to provide a solution for low phase noise oscillator in microwave circuits.

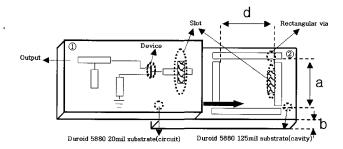
Key words: Planar Cavity Resonator, Microstrip Resonator, HBT, MEMS, MMIC.

#### I. Introduction

The number of radio frequency applications has steadily increased with demanding for more bandwidth and higher data-rates which are shifting the carrier frequencies to higher values. A Ku-band has been used in a satellite communication since the end of 1970'. In the satellite communication, the low phase noise frequency synthesizer is essential element to implement high quality communication link.

Both the low 1/f noise of a device and the high quality factor of a resonator are very important factors to reduce the phase noise. SiGe HBT has good characteristics of low-frequency 1/f noise compared to other devices such as MESFET and HEMT<sup>[1]~[3]</sup>. Therefore, the SiGe HBT(LPT16ED) produced by SiGe semiconductor, Inc. is chosen. Many kinds of the resonators have studied to reduce the phase noise. Dielectric resonators have been widely used for low phase noise, but it can not be used monolithic microwave integrated circuit. microstrip resonators have the advantages such as high integration, low cost and simply manufacturing. However it can not mainly affect to reduce the phase noise of oscillator, due to the low O of the resonator. Therefore, the micromachined cavities of the planar type have be developed<sup>[4]~[6]</sup>. This resonator is asked to have a high skill manufacturing process and the high cost of the beginning development stage.

In Fig. 1, planar cavity resonator(PCR) oscillator consists in two kinds of the substrates(duroid 5880,  $\varepsilon_r$  =2.2) which are different from thickness. The circuit is



Up substrate: Duroid 5880, Er=2.2. thickness=0.5 [mm]
 Bottom substrate (PCR): Duroid 5880, Er=2.2, thickness=3.175 [mm]

Fig. 1. The topology of the suggested PCR oscillator.

fabricated on the up substrate and the PCR on the bottom substrate. As the rectangular via is in the role of the cavity's metallic wall, the size of the PCR is decided by length d, width a and height b. Coupling between cavity and the microstrip line is achieved with the slot on the ground plane of the microstrip line.

The main contributions of this PCR are following. First, we can obtain the higher Q of the resonator than that of other microstrip resonators. Second, it has the advantage of a planar form which is easy to integrate with microwave integrate circuit(MIC). Because the cavity substrate is placed at the bottom of circuit substrate, it is easy to make integration better. Third, there is easy-fabrication process, because of the conventional PCB processing.

## II. Selection of the Optimum Cavity Size

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When the wanted cavity resonator is designed, the quality factor(Q) and the mode characteristic of the resonator must be taken into account. Through the following design process, the excitation of spurious modes is minimized and the Q is maximized. The higher the Q of the resonator is, the more stable the oscillator can be. The adjacent mode of the dominant mode should be as far as possible to increase the selectivity of oscillator frequency. It is relative to the dimension of resonator. In Fig. 1, it consists of the length d, the width d and the height d of cavity's size.

$$f_{nml} = \frac{ck_{mnl}}{2\pi\sqrt{\mu_{r}\varepsilon_{r}}} = \frac{c}{2\pi\sqrt{\mu_{r}\varepsilon_{r}}} \sqrt{\left(\frac{m\pi}{a}\right)^{2} + \left(\frac{n\pi}{b}\right)^{2} + \left(\frac{l\pi}{d}\right)^{2}}$$

$$Q_{o} = \frac{2\omega_{0}W_{e}}{P_{c}} = \frac{(kad)^{3}b\eta}{2\pi^{2}R_{s}(2l^{2}a^{3}b + 2bd^{3} + l^{2}a^{3}d + ad^{3})}$$
(2)

At equation (1), when the TE<sub>101</sub> of the dominant mode is 12.5 GHz, we can have the union of a and d in  $11.5 \le d \le 25$  mm, b=3.175 mm in Table 1. According to this union, we can again get the harmonics at the dominant frequency 12.5 GHz from equation (1).

Fig. 2 shows the mode chart for a rectangular cavity with the variation of length a and d, when TE<sub>101</sub> mode resonant frequency is 12.5 GHz at b=3.175 mm. The farthest distance between the dominant mode(TE<sub>101</sub>) and the second harmonic(TE<sub>102</sub>) must be selected to minimize the excitation of spurious modes. Simultaneously, the Q of the resonator should be taken into account. The unloaded  $Q_O$  can be calculated on equation (2). Fig. 3 shows the relation of the unloaded  $Q_O$  and the second harmonic (TE<sub>102</sub>).

As you can see in Fig. 3, the farther the distance between the dominant and the second harmonic is, the higher the unloaded  $Q_O$  of the resonator is. Therefore, the relation of the unloaded  $Q_O$  and the spurious is

Table 1. According to the size, the second harmonic at f=12.5 GHz. (b=3.175 mm)

d [mm]	a [mm]	Dominant Frequency[GHz]	Second Frequency [GHz]
11.5	11.38459	12.5	19.7
11.8	11.11446	12.5	19.4
12	10.95499	12.5	19.2
12.3	10.74143	12.5	18.9
	•••		•••
22.1	8.69417	12.5	14.8
22.5	8.67056	12.5	14.7
22.9	8.64835	12.5	14.7

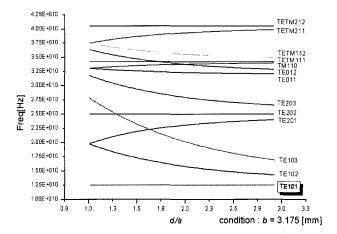


Fig. 2. Mode chart for a rectangular cavity.

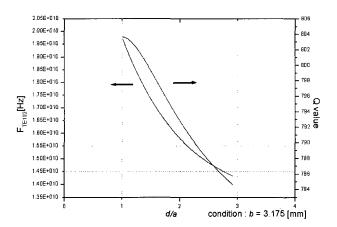


Fig. 3. The relation of the Q and second harmonic at b = 3.175 [mm].

proportioned. When the d=11.5 [mm] and a=11.38 [mm] are selected, the second harmonic is at 19.7 GHz. At this point, the excitation of spurious modes is minimized and the unloaded  $Q_0$  is maximized. Therefore, the initial size of the cavity can be decided. These results are verified with the eigemode solver of the FEM simulator(HFSS).

#### III. Slot Position

The resonator of the band stop mode must be designed for this topology of the series feedback oscillator. So the coupling between the cavity and the microstrip is occurred at H-field in this resonator. Using full wave field simulation used to the FEM simulator (ansoft HFSS), we can verify the H-field distribution of the cavity. The high H-field distribution is at the near rectangular via in Fig. 4. Therefore, this slot position is selected at the high H-field distribution to get the high coupling.

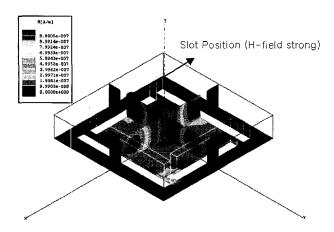


Fig. 4. The H-field distribution of the cavity.

# IV. Resonator Parameter according to the Slot Size

The important parameters of the resonator such as unloaded  $Q_O$ , loaded  $Q_L$  and coupling coefficient K are analyzed according to the slot size. The magnitude of the  $S_{11}$ (input reflection at resonator) is important to gratify the oscillation condition. In the conventional DR resonator, these parameters are decided by the distance between the dielectric resonator and the microstrip<sup>[7]</sup>. But the parameter of this PCR is decided to the size of the slot, due to the fixed circuit thickness. As the slot length is varied from 2.0 to 4.0 mm with varying the slot width from 0.1 to 2.5 mm, these parameters are extracted by using the FEM simulator.

# 4-1 Unloaded Qo and Loaded QL

The average value of the unloaded  $Q_0$  is 750. According to the large size of the slot, the unloaded  $Q_0$  is varied largely. In Fig. 5, we can see that the

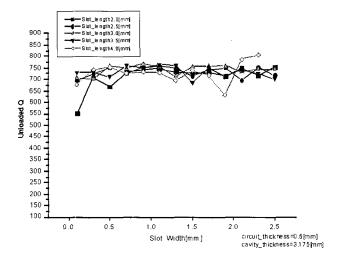


Fig. 5. Unloaded  $Q_0$  according to the slot size.

maximum point is 802 and the minimum point is 550.

In Fig. 6, the values of the loaded  $Q_L$  are in inverse proportion to the size of the slot. It is clear that the relation of the k and  $Q_L$  is as like in equation (3). The maximum point is 600 and the minimum point is 10.

#### 4-2 Coupling Coefficient

The coupling coefficient is the measurement of the degree of coupling between the cavity and the transmission line. When K=1, this is a critical coupling. For K<1, the cavity is undercoupled to the external component. The cavity and the external component are overcoupled when K>1. In Fig. 7, the larger the slot size is, the bigger the coupling coefficient is. The loaded  $Q_L$  and the unloaded  $Q_O$  are related by the coupling coefficient. The relationship is

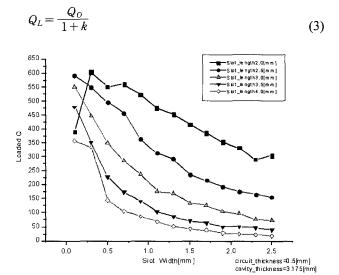


Fig. 6. Loaded  $Q_L$  according to the slot size.

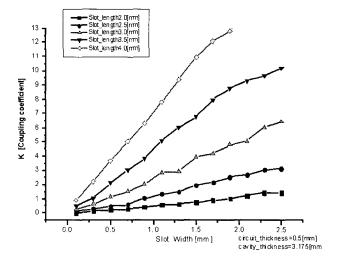


Fig. 7. Coupling coefficient according to the slot size.

If the high k is selected, the loaded  $Q_L$  is low, and then the phase noise is degraded. If the lowest K to get the highest loaded  $Q_L$  is selected, the oscillator condition in equation (4) can not be satisfied. We need to trade off between the loaded  $Q_L$  and the coupling coefficient K at the highest unloaded  $Q_O$ .

#### 4-3 Magnitude of S<sub>11</sub>

When the oscillator is designed, we must think about the magnitude of the  $S_{11}$ (input reflection at resonator) to satisfy with the initial condition of the oscillator.

$$|S_{11}| > \frac{1}{|S_{11}|}, phase(S_{11}) = -phase\left(\frac{1}{|S_{11}|}\right)$$
 (4)

Because the  $S_{11}$ ' of this device is  $1.45 \angle -168.492$ ,  $S_{11}$  can be calculated in equation (4). The magnitude of  $S_{11}$  must be selected at the upper side of 0.68 in Fig. 8. So the size of a slot is decided to select the 2.5 mm of slot length and the 1.6 mm of slot width.

# V. Designed PCR and Measurement Data

Using the general PCB processing, the planar cavity resonator is manufactured in Fig. 9. Cavity substrate is duriod 5880 which has  $\varepsilon_r = 2.2$  and thickness=3.175 mm. In Fig. 10, there is the difference between the simu-

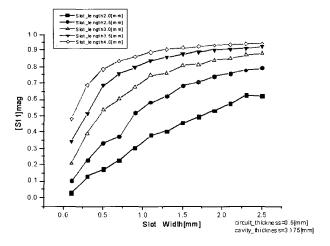


Fig. 8. The magnitude of  $S_{11}$  according to the slot size.

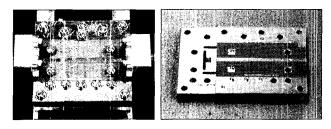
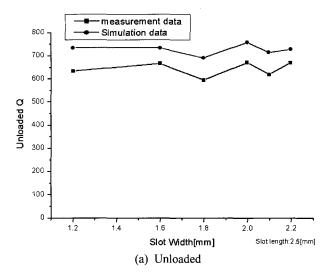
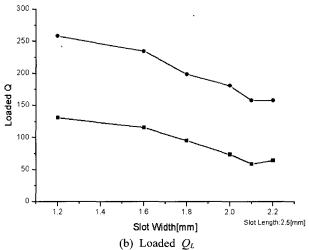


Fig. 9. Photo of designed planar cavity resonator.





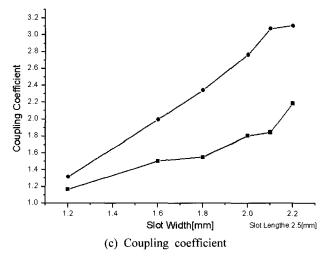


Fig. 10. Measured the parameter of the PCR.

lation and measurement data. It seems to generate the manufacture error and the mismatch of the slots between microstrip and cavity, so it was again simulated according to the change of cavity size for getting the amount of the resonance frequency shift. As the width of cavity reduces 0.02 mm, the frequency is shifted about +10 MHz. In Fig. 10(b), (c), the average of the unloaded  $Q_O$  is 650. The measurement loaded Q is 100 worse than the simulation data.

As mentioned in the previous chapter, the slot width 1.6 mm and length 2.5 mm are selected. In Fig. 11 and Table 2, the simulation data is compared with the measurement data. The Q values of the unloaded and loaded PCR is measured 667.9 and 115.6. The frequency is shifted by the manufacturing error.  $Q_L$  is high error as compared with the  $Q_0$ , due to the slot position error. When the performance of the planar cavity resonator is compared with the existing resonator in Table 3, the loaded Q of the DR is the highest value among them. But the DR is the nonplanar structure. The MSR has the advantage of planar structure. But the MSR has the low unloaded Q, due to the radiation and dielectric loss. MEMS resonators using micromaching techniques to eliminate the dielectric loss have the unloaded Q of 500. But it needs to the high processing skill. As a result, the unloaded Q of the planar cavity resonator is much better than that of MSR and MEMS resonator. Although the Q of PCR is lower than that of the DR, there are merits of the easy fabrication, low cost, and the integration capability.

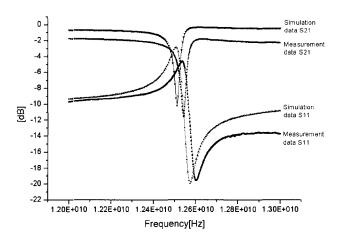


Fig. 11. Simulation and measurement data of designed PCR.

Table 2. The PCR parameter of the simulation and measurement.

	Simulation	Measurement
Frequency	12.51 GHz	12.51 GHz
K	2.3387	1.5
Unloaded Q	691.4365	667.9
Loaded Q	198.8086	115.6

Table 3. The comparison of different resonators.

	Туре	Frequency[GHz]	Unloaded Qu
MSR	Hair-pin resonator <sup>[10]</sup>	9	170
	Spiral resonator <sup>[11]</sup>	10	146
MEMS	Micromachined cavity resonator <sup>[6]</sup>	10.285	506
	Membrane resonator <sup>[5]</sup>	28.7	460
DR	DR resonator <sup>[12]</sup>	9.6	5000
PCR	This work	12.56	667.9

# VI. Designed Oscillator Using PCR

The topology of oscillator is used for the common emitter series feedback. The series feedback is better than the parallel feedback about low phase noise<sup>[8]</sup>. Another oscillator is designed by using a conventional MSR( $\lambda$ /4 open stub resonator) to investigate the performance. The same circuit substrate(duroid 5880, Er =2.2, thickness=0.5 mm) and the same device(LPT16-ED) are used.

Fig. 12 shows the designed PCR oscillator. Two substrates are aligned by several halls and are fixed by several screws. The performance of PCR oscillator is measured by the test fixture. When a RF cable loss is considered, the output power of the oscillator was 2.76 dBm at  $Ib(\text{Tr base current})=497~\mu\text{ A}$  and  $Vce(\text{Tr currect})=497~\mu\text{ A}$  and Vc

When the base current is swept from 0.3 to 0.7 mA at *Vce*=3 V, the base current dependence of the output power, frequency deviation and phase noise is shown in Fig. 16, Fig. 17. This PCR oscillator is stable within the range of the base current which varies from 0.3 mA to 0.55 mA. In Table 4, measurement shows reduced phase noise by 17.61 dB dBc/Hz @1 MHz offset in the oscillator



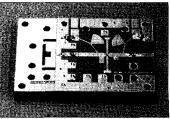


Fig. 12. Photo of designed PCR oscillator.

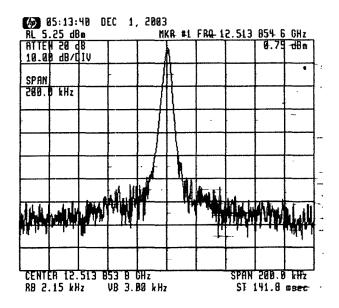


Fig. 13. Measured data of PCR oscillator at span 200 kHz.

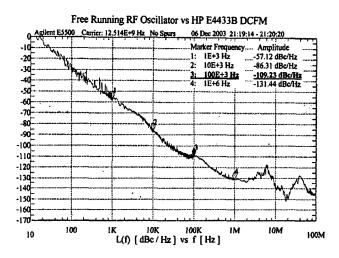


Fig. 14. Measured phase noise of PCR oscillator.

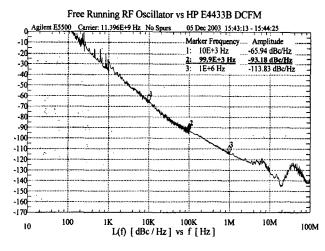


Fig. 15. Measured phase noise of MSR oscillator.

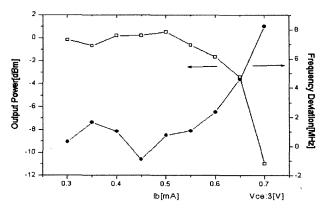


Fig. 16. Base bias dependence of PCR oscillator outpower and frequency deviation from 12.5 GHz.

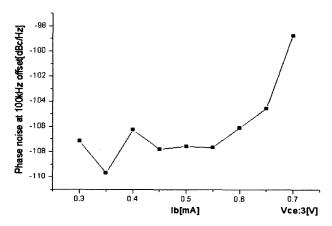


Fig. 17. Base bias dependence of PCR oscillator phase noise from 12.5 GHz.

Table 4. Measured the parameters of oscillator (lb=497  $\mu$  A, Vce=3 V).

		Measured data	
		PCR	MSR
Frequency [GHz]		12.51	11.39
Output Power [dBm]		2.767	-1.472
Second harmonic suppress [dBc]		-35.02	-25.5
	10 kHz	-86.31	-65.95
Phase noise [dBc/Hz]	100 kHz	-109.23	-93.18
	1 MHz	-131.44	-113.83

with the PCR compared to the conventional microstrip resonator(MSR) in Fig. 15.

In Table 5, the PCR oscillator is compared with the published paper. In the SiGe device, the phase noise of the PCR oscillator shows the reduction of phase noise by 11 dBc/Hz @100 kHz offset compared to that of the

Table !	5.	The	phase	noise	of	published	oscillator.
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	Frequency	Output- power	Phase noise (@100 kHz)
DR <sup>[9]</sup>	12.7 GHz	-10 dBm	-124 dBc/Hz
Hair-pin <sup>[10]</sup>	9 GHz	5 dBm	-92 dBc/Hz
PCR	12.514 GHz	2.767 dBm	109.23 dBc/Hz

published hair-pin oscillator[10].

## VII. Conclusions

In this paper, a design method for oscillator using the planar cavity resonator is investigated, fabricated and tested to obtain the low phase noise oscillator utilizing hybrid integration on all planar printed circuit board.

The output power of the oscillator is 2.76 dBm and the phase noise is -109.23 dBc/Hz @100 kHz offset. Although this PCR oscillator shows higher phase noise than DR oscillator, the phase noise level of the PCR oscillator is lower than that of the MSR oscillator and the oscillator based on MEMS resonator<sup>[6]</sup>.

That is, 1) The PCR oscillator makes an integration circuit better than the DR oscillator. 2) The fabrication processing of the PCR has easier than that of the MEMS resonator. 3) The Q of PCR is higher than that of MSR. Therefore, PCR shows a noticeable reduction of phase noise and expects to the possibility of the easy fabrication and inexpensive mass production.

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